

# Computer science (CS) aspects of the fast multipole method

Rich Vuduc

Aparna Chandramowlishwaran (UC Irvine)  
Jee Choi (Georgia Tech), Kamesh Madduri (Penn State)  
+ many collaborators!

Georgia  
Tech



College of  
Computing

Computational Science and Engineering



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Far field?



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Rahimian  
NYU/Courant

Lashuk Tufts

Biros UT Austin

Chandramowliswaran  
UC Irvine



← A CS casualty of the FMM

There is **good** news and **bad** news.‡

‡ ... with CS research questions, not math questions



*Bad news?*



*Bad news?* **We may never be truly done.**‡

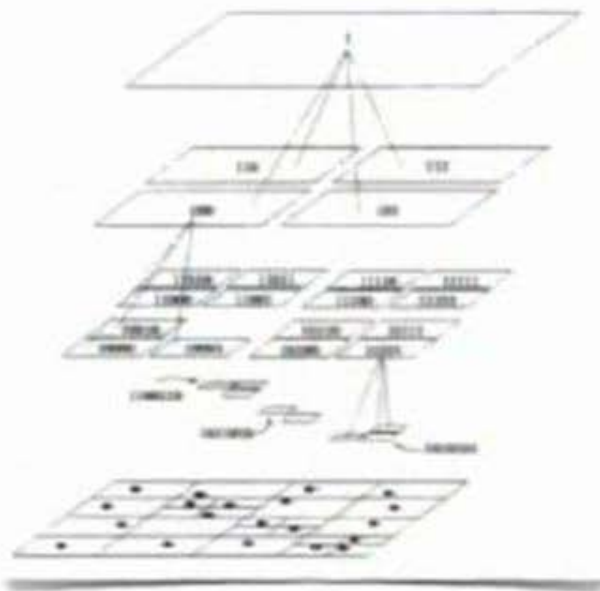
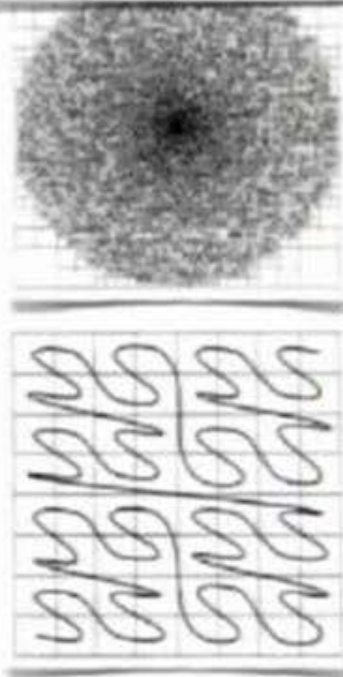
‡ At least not until we close the **performance engineering gap**.

## A Parallel Hashed Oct-Tree N-Body Algorithm

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John K. Salmon  
 Physics Department  
 206-49  
 California Institute of Technology  
 Pasadena, CA 91125

<i>computation stage</i>	<i>time (sec)</i>
Domain Decomposition	7
Tree Build	7
Tree Traversal	33
Force Evaluation	54
Load Imbalance	7
Total (5.8 Gflops)	114



At later stages of the calculation the system becomes extremely clustered (the density in large clusters of particles is typically  $10^6$  times the mean density). The number of interactions required to maintain the same accuracy grows moderately as the system evolves. At a slightly increased error bound of  $4 \times 10^{-3}$ , the number of interactions in the clustered system is  $2.6 \times 10^{10}$  per timestep.

<i>computation stage</i>	<i>time (sec)</i>
Domain Decomposition	19
Tree Build	10
Tree Traversal	55
Data Communication during traversal	4
Force Evaluation	60
Load Imbalance	12
Total (4.9 Gflops)	160



# A Parallel Hashed Oct-Tree Network scaling

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 Pasadena, CA 91125



At later stages of the calculation the system becomes extremely clustered (the density in large clusters of particles is typically  $10^6$  times the mean density). The number of interactions required to maintain the same accuracy grow moderately as the system evolves. At a slightly increased error bound of  $4 \times 10^{-3}$ , the number of interactions in the clustered system is  $2.6 \times 10^{11}$  per timestep.

operation name	time (secs)
Domain Decomposition	~
Tree Build	~
Tree Traversal	23
Force Evaluation during Traversal	6
Force Evaluation	24
Load Imbalance	~
Total (5.3 Gflops)	114

operation name	time (secs)
Domain Decomposition	19
Tree Build	10
Tree Traversal	55
Data Communication during traversal	4
Force Evaluation	74
Load Imbalance	12
Total (4.9 Gflops)	161

$$T_{\text{compute}} + T_{\text{network}} + T_{\text{memory}}$$

# PROVABLY GOOD PARTITIONING AND LOAD BALANCING ALGORITHMS FOR PARALLEL ADAPTIVE N-BODY SIMULATION\*

SHANG-HUA TENG†

---

**THEOREM 5.1.** *Let  $G$  be a weighted  $N$ -body communication graph (for either BH or FMM) of a set of particles at  $P = \{\mathbf{p}_1, \dots, \mathbf{p}_n\}$  in  $\mathbb{R}^d$  ( $d = 2$  or  $3$ ). If  $P$  is  $\mu$ -nonuniform, then  $G$  can be partitioned into two equally weighted subgraphs by removing at most  $O(n^{1-1/d}(\log n + \mu)^{1/d})$  nodes, or by removing edges of at most  $O(n^{1-1/d}(\log n + \mu)^{2+1/d})$  total edge weights.*

Recursively applying our partitioning theorem, we can analyze the quality of the recursive bisection scheme for  $p$ -way partitioning. (See Simon and Teng [27] for unstructured meshes.)

**COROLLARY 5.2.** *If  $G$  is a (weighted)  $N$ -body communication graph for particles that are  $\mu$ -nonuniform, then  $G$  can be partitioned into  $p$  equally weighted subgraphs such that the total weight of the removed edges is bounded by  $O(p^{1/d}n^{1-1/d}(\log n + \mu)^{2+1/d})$ .*

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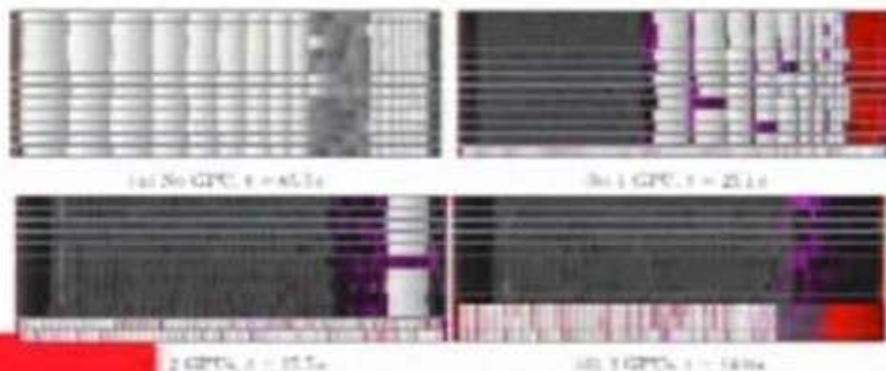


Figure 14: Simulation trace on Nebulem-Primo (X2070) for an ellipsoid test case ( $N = 30 \cdot 10^3$ ,  $\Delta x = 0.5$ ,  $\Delta y = 0.5$ ,  $\omega_p = 1000$ ) using 0 to 3 GPUs. We also report the execution time  $t$ .



## Task-based FMM for heterogeneous architectures

Emmanuel Agullo, Béranger Brunsch, Olivier Coulaud, Eric Darve, Matthias Messmer, Taro Takahashi

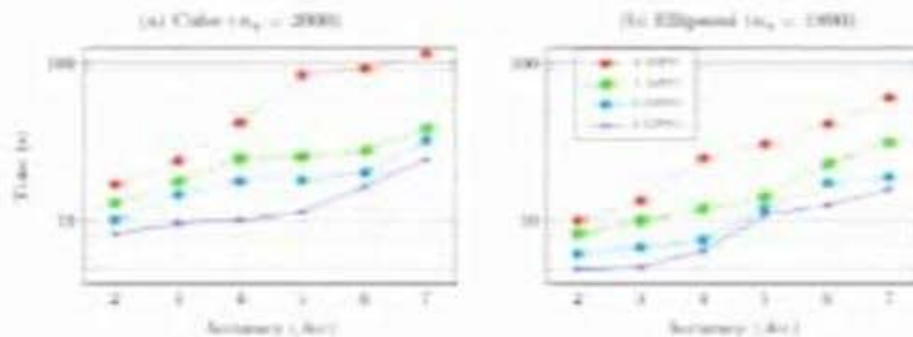
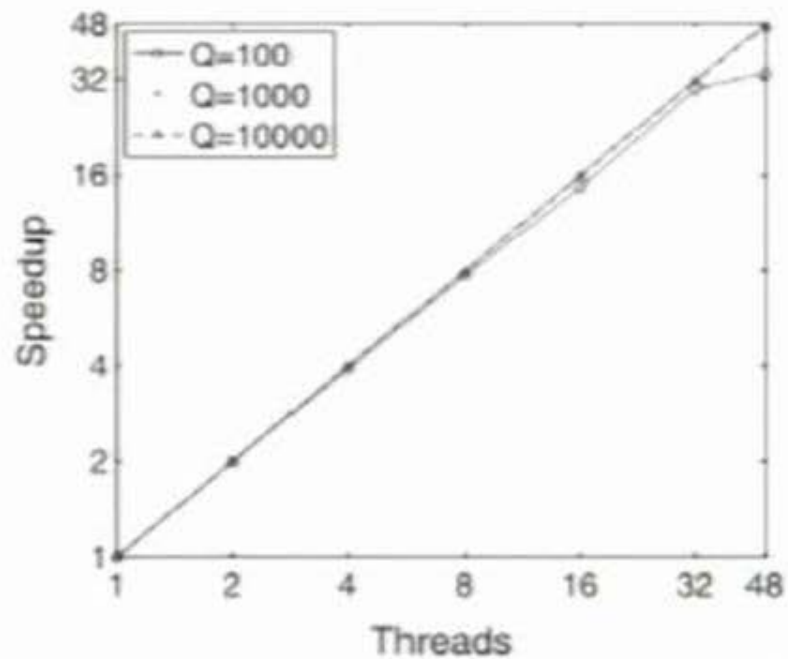
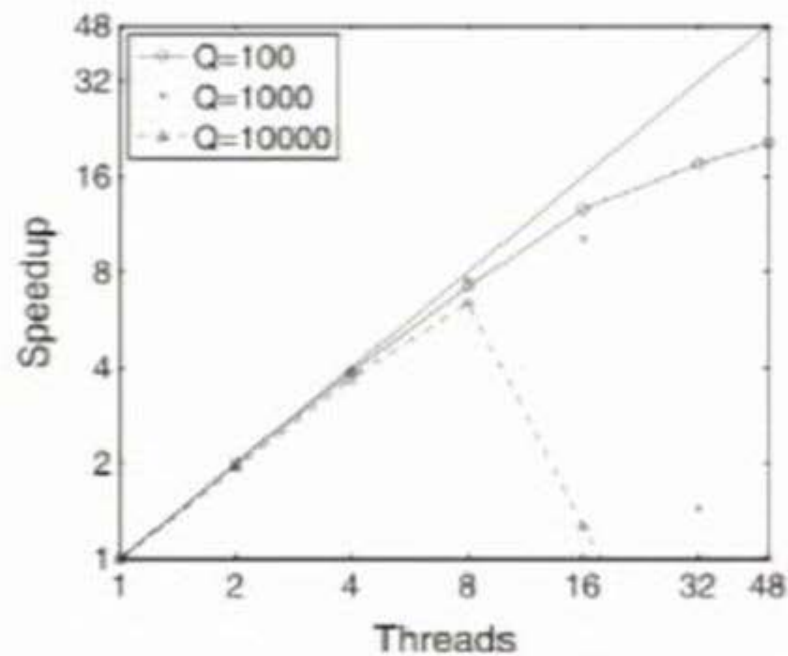
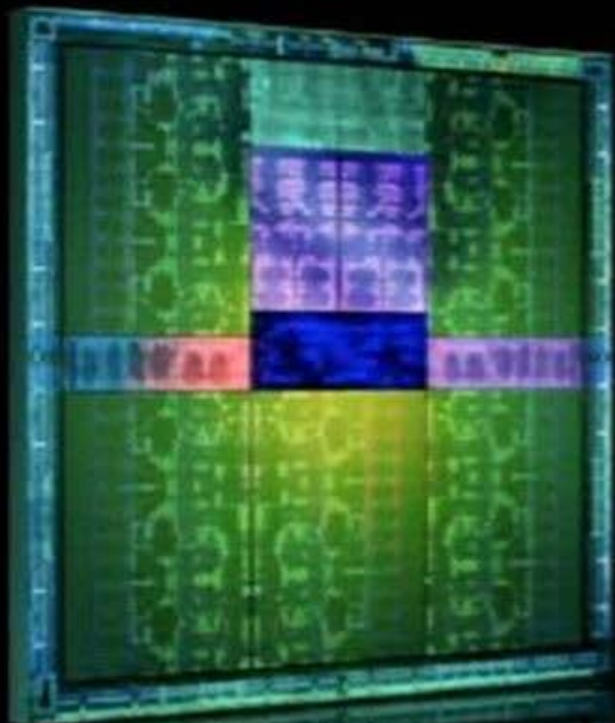


Figure 16: Time to completion (log scale) for distributions of  $N = 30 \cdot 10^3$  particles. For a given accuracy and number of GPUs, the tree height  $k$  minimizing the time to completion was selected.

# Data-driven execution of fast multipole methods

Hatem Ltaief<sup>1,\*</sup> and Rio Yokota<sup>2</sup>





## The World's Most Powerful GPU

**2688**

CUDA Cores

**4500**

Gigaflops

**7.1**

Billion  
Transistors



# Fast multipole methods on graphics processors

Nail A. Gumerov\*, Ramani Duraiswami

Perceptual Interfaces and Reality Laboratory, Computer Science and UMIACS, University of Maryland, College Park, United States  
Fastalgo, LLC, Elkridge, MD, United States

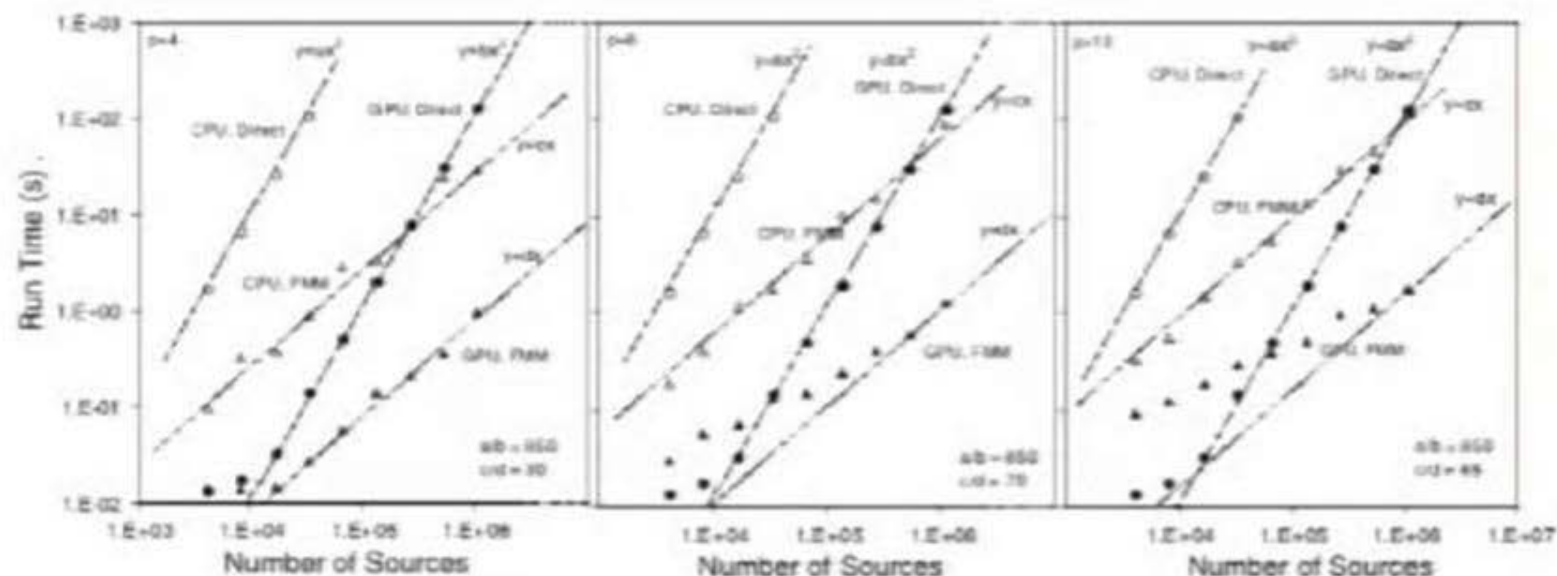
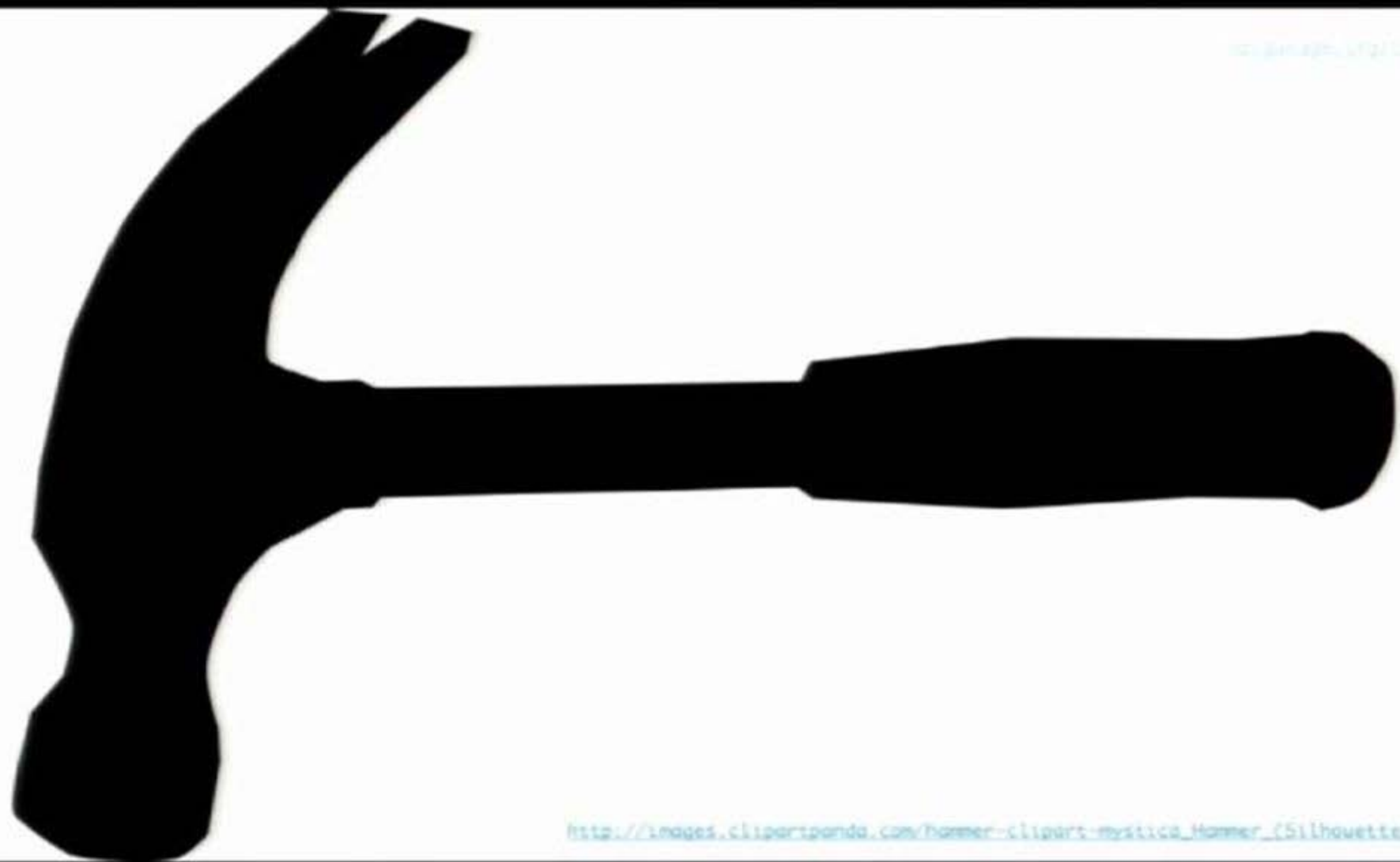


Fig. 11. FMM wall clock time (in seconds) for serial CPU code (one core of 2.67 GHz Intel Core 2 extreme QX is employed) and for GPU (NVIDIA GeForce 8800 GTX) for different truncation numbers  $p$  (potential+gradient). Also direct summation timing is displayed for both architectures. No SSE optimizations for the CPU were used.

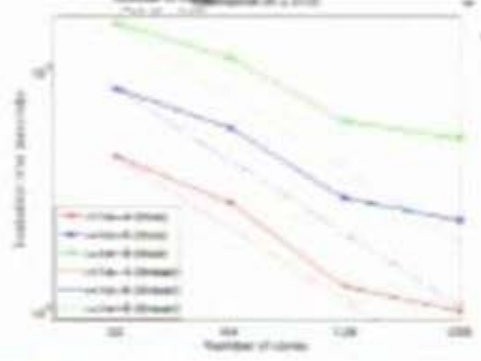
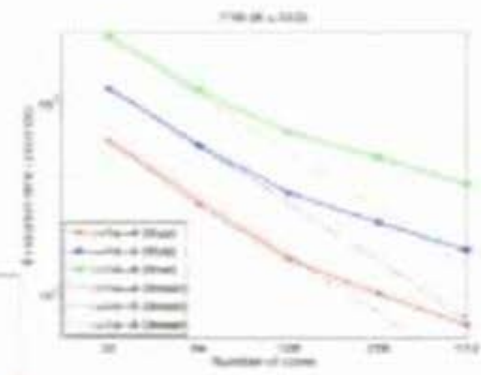
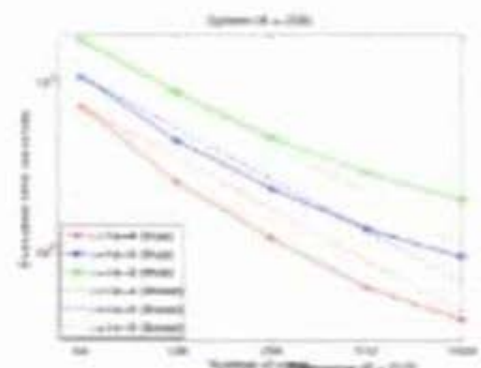
<http://images.clipartpanda.com>



[http://images.clipartpanda.com/Hammer-clipart-mystica\\_Hammer\\_\(Silhouette\).png](http://images.clipartpanda.com/Hammer-clipart-mystica_Hammer_(Silhouette).png)

# A PARALLEL DIRECTIONAL FAST MULTIPOLE METHOD

AUSTIN R. BENSON\*, JACK POULSON†, KENNETH TRAN‡,  
BJÖRN ENGQUIST§, AND LEXING YING¶

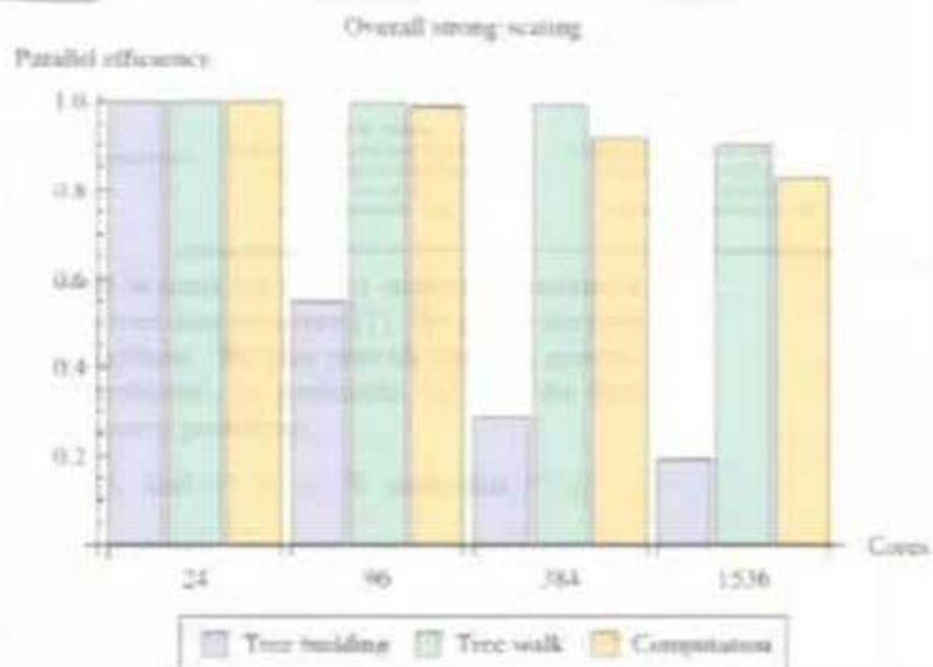
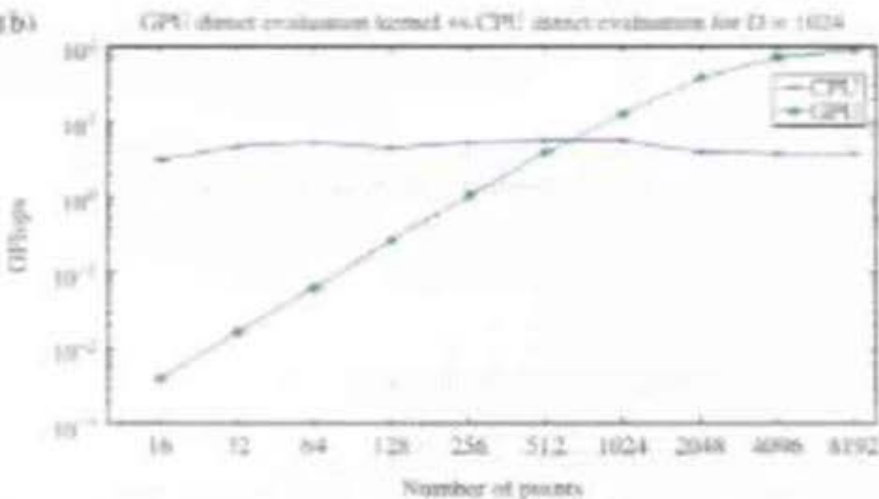


# A Distributed Kernel Summation Framework for General-Dimension Machine Learning

Dongyeon Lee<sup>1\*</sup>, Piyush Sae<sup>2</sup>, Richard Vuduc<sup>2</sup> and Alexander G. Gray<sup>2</sup>

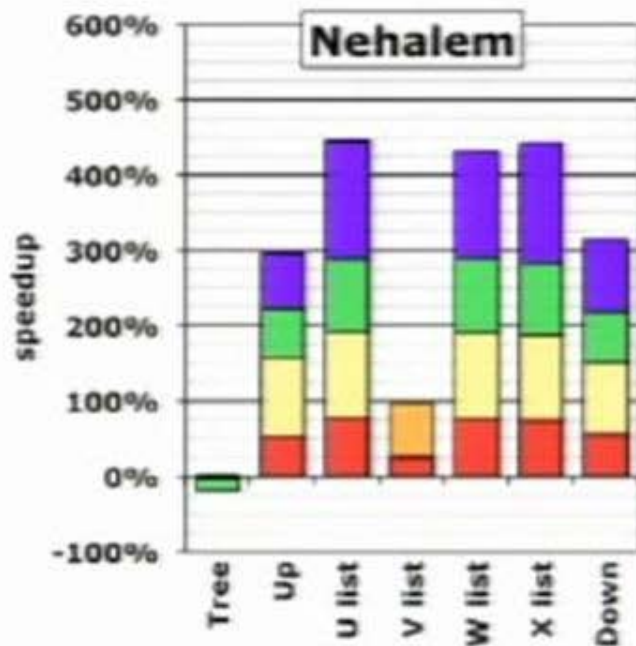
<sup>1</sup>GE Global Research, Schenectady, NY 12309, USA

<sup>2</sup>Computational Science and Engineering, Georgia Institute of Technology, Atlanta, GA 30332, USA





Aparna



+SIMDization

+Newton-Raphson  
Approximation

+Structure of Arrays

+Matrix-Free  
Computation

+FFTW

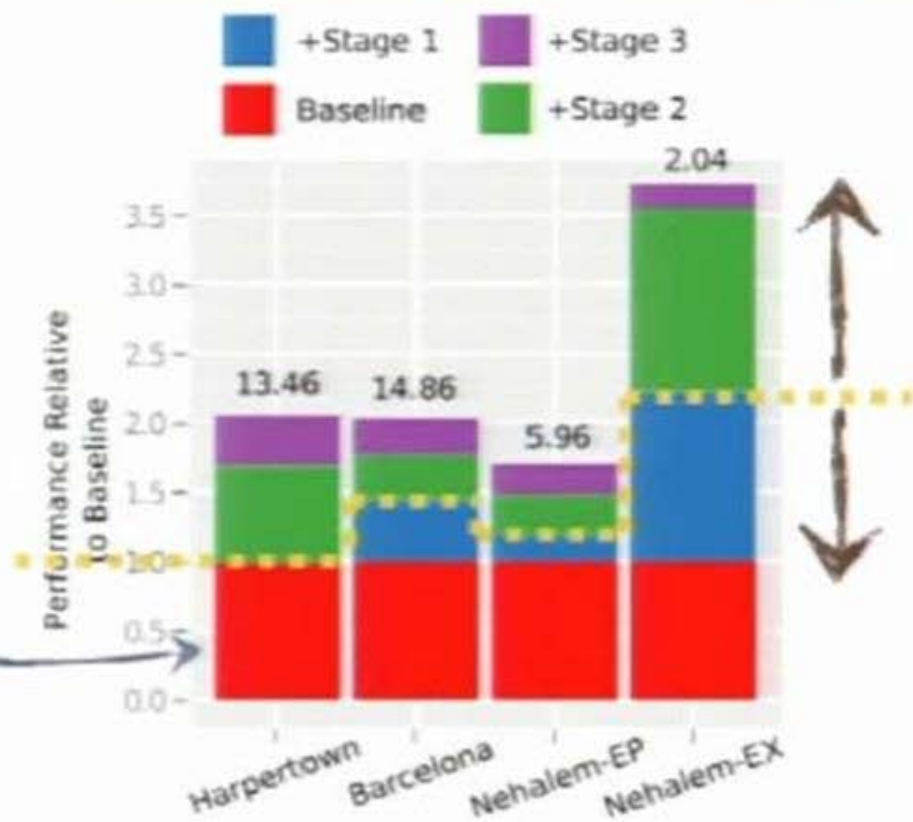
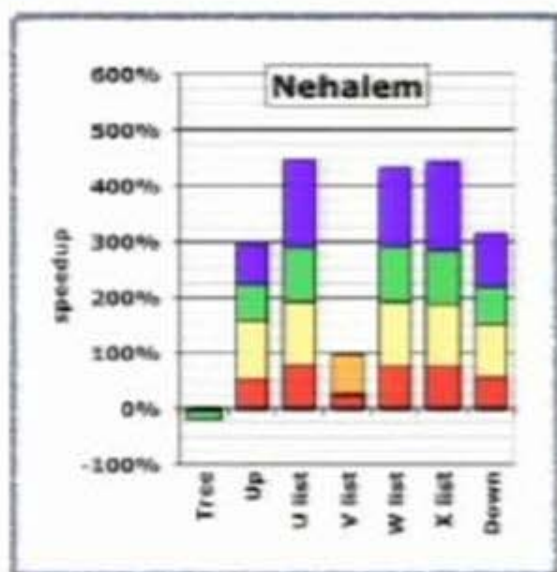
Problem-specific performance  
engineering

Assume full knowledge of data access patterns, algorithms, and code



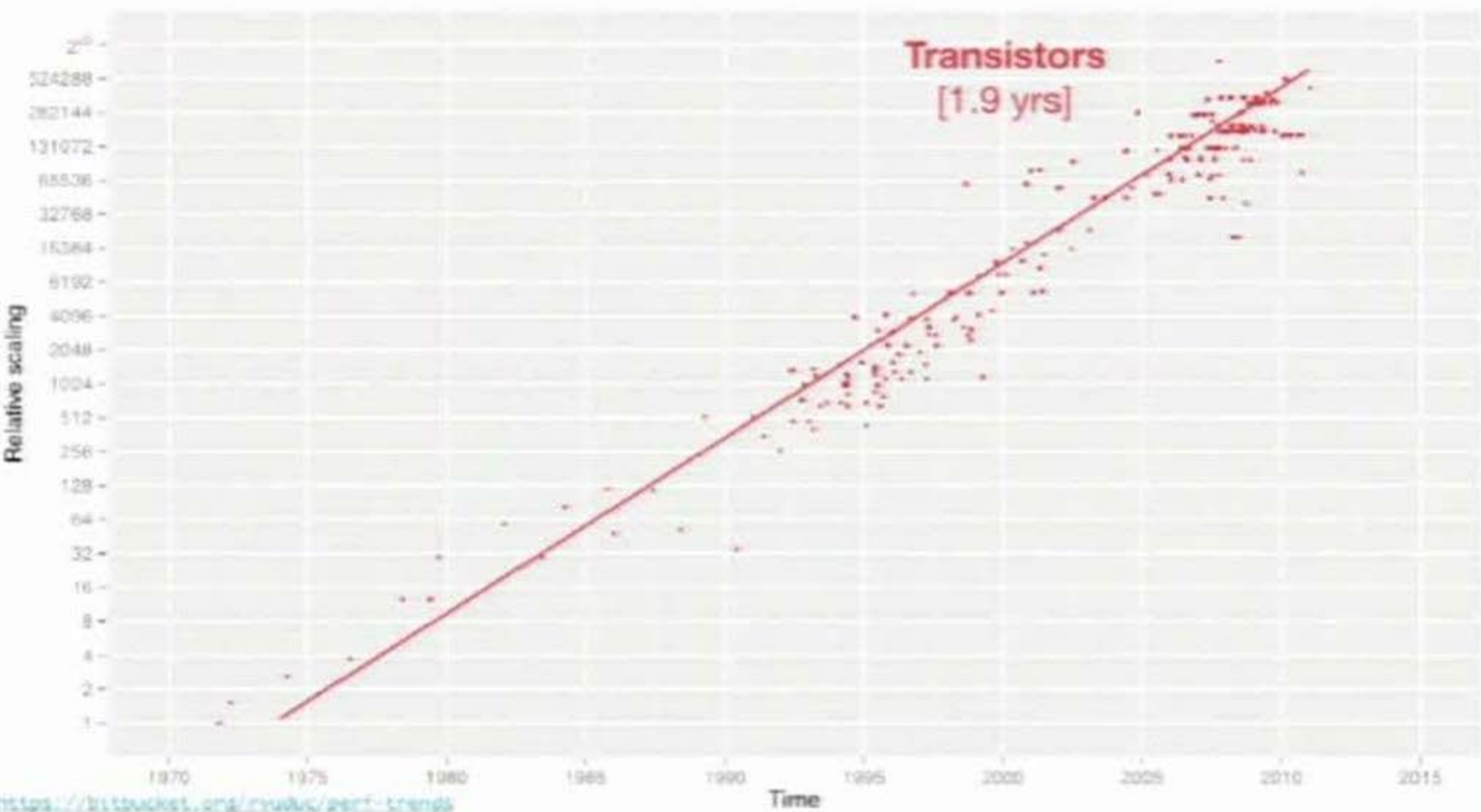


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Problem-specific performance engineering

Assume full knowledge of data access patterns, algorithms, and code

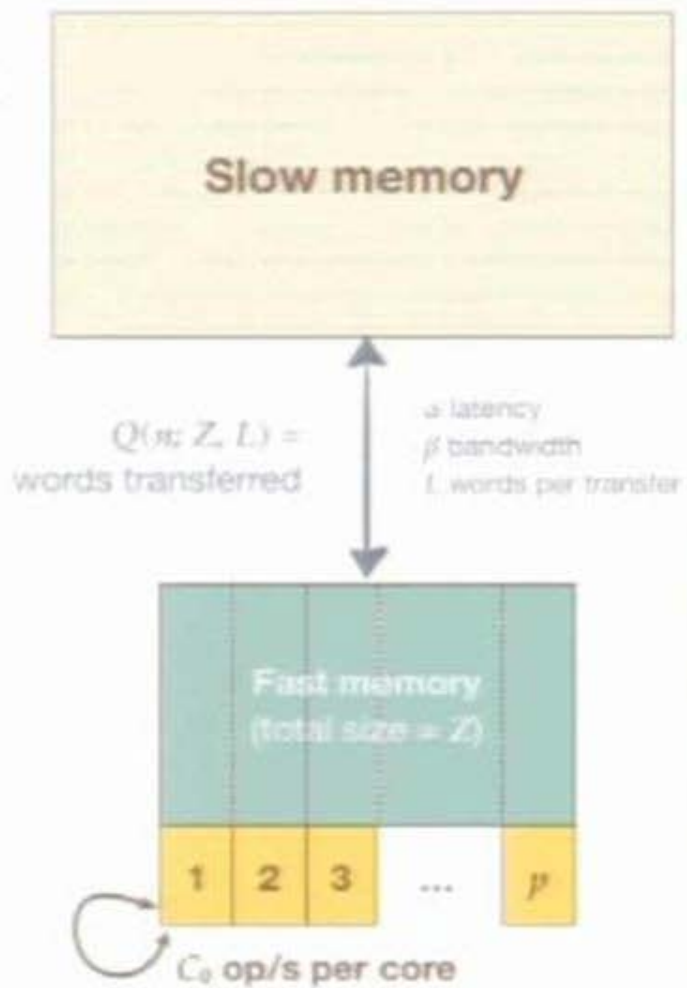


<https://bitbucket.org/rvwk/perf-trends>



Aparna

See Aparna's SPAA'12 brief announcement:  
Communication analysis of the fast multipole method





Aparna

See Aparna's SPAA'12 brief announcement:  
Communication analysis of the fast multipole method

~ accuracy

$$T \propto \frac{n \Delta \text{ bytes}}{p \cdot C_0} \left( 1 + (\text{const.}) \frac{p \cdot C_0}{\beta} \right)$$

Min. time  
(ops only)

Communication penalty:  
**processor balance**  
(a.k.a., flop:byte)



$Q(n; Z, L) =$   
words transferred

$\alpha$  latency  
 $\beta$  bandwidth  
 $L$  words per transfer



$C_0$  op/s per core

*A summary?*

Despite tremendous progress, there is a hidden cost, caused by the lack of tools and *simple* techniques to make fast code persist.